

**IN THE ABSTRACT**

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### **ABSTRACT**

A driver circuit (20) including a differential first pair of transistors (Q1, Q2), a voltage drive stage (26) including a second pair of transistors (Q3, Q4), a first pair of nodes (N1, N2) coupled to the first pair of transistors (Q1, Q2) and to the second pair of transistors (Q3, Q4), a second pair of nodes (N3, N4) coupled to the second pair of transistors (Q3, Q4), and a dynamic current switch (28) coupled to the second pair of nodes.